

App. No. 10/604,065  
Response dated September 28, 2005  
Reply to Office Action of June 29, 2005

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the present application.

**Listing of Claims:**

**Claim 1 (currently amended):** A wafer holder for semiconductor manufacturing devices, the wafer holder comprising:  
~~the wafer holder having~~ a wafer-carrying surface; ~~and comprising:~~  
an electrical circuit formed either on a surface other than the wafer-carrying surface of the wafer holder, or else inside it; and  
electrodes for supplying power to said electrical circuit, said electrodes being separated by an interval that is 10% or more of the thickness of the wafer holder such that in use the wafer-carrying surface has a temperature uniformity of within  $\pm 1$  percent.

**Claim 2 (original):** The wafer holder set forth in claim 1, wherein one or more metals selected from the group consisting of tungsten, molybdenum and tantalum are incorporated into portions of said electrodes that are directly connected to said circuit.

**Claim 3 (original):** A semiconductor manufacturing device wherein the wafer holder set forth in claim 1 is installed.

**Claim 4 (original):** A semiconductor manufacturing device wherein the wafer holder set forth in claim 2 is installed.

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**Claim 5 (new):** A wafer holder for semiconductor manufacturing devices, the wafer holder comprising:

a wafer-carrying surface;

a plurality of electrical circuits formed either on a surface other than the wafer-carrying surface of the wafer holder, or else inside it; and

at least three electrodes for supplying power to said plurality of electrical circuits, each pair of electrodes being separated by an interval that is 10% or more of the thickness of the wafer holder, such that in use the wafer-carrying surface has a temperature uniformity of within  $\pm 1$  percent.